

LATCH TYPE SENSE AMPLIFIER METHOD AND APPARATUS

ABSTRACT OF DISCLOSURE

Disclosed is an apparatus for and a method of overcoming
5 signal delay problems in a read-out path occurring in connection
with pipelined memory circuits. A latch type sense amplifier
(SA) is used to receive the memory cell logic levels during a
pre-charge state in a cycle prior to read-out. Thus, the SA may
quickly provide an output signal during a read latch clock
10 cycle. The SA output is passed through a dynamically enabled
logic circuit to a latch circuit for holding the receiving logic
value for use in the next clock cycle.